

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Isamu HAYASHI et al.

Appln. No.: 09/897,902

Group Art Unit: 2112

Filed: July 5, 2001

Examiner: C. Lee

For: MICROPROCESSOR, SEMICONDUCTOR MODULE AND DATA

PROCESSING SYSTEM

AMENDMENT

RECEIVED

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 JUL 3 0 2004 Technology Center 2100

Sir:

In response to the Office Action mailed on <u>January</u>

26, 2004, please amend the above-identified patent

application as indicated below.

A Request for Extension of Time is filed concurrently herewith.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 16 of this paper.